

Errata

<Document Name>

CMOS-6/6A/6S/6V/6X Family Design Manual Ver.11.0 User's Manual

<Document No., Date Published>

A10137EJCV0UM00 (12th edition) , December 1998 N

<Location of Error>

p.204 6.3.5 Testing multifunction I/O circuits (1) Oscillator circuits (Under development)

<Description>

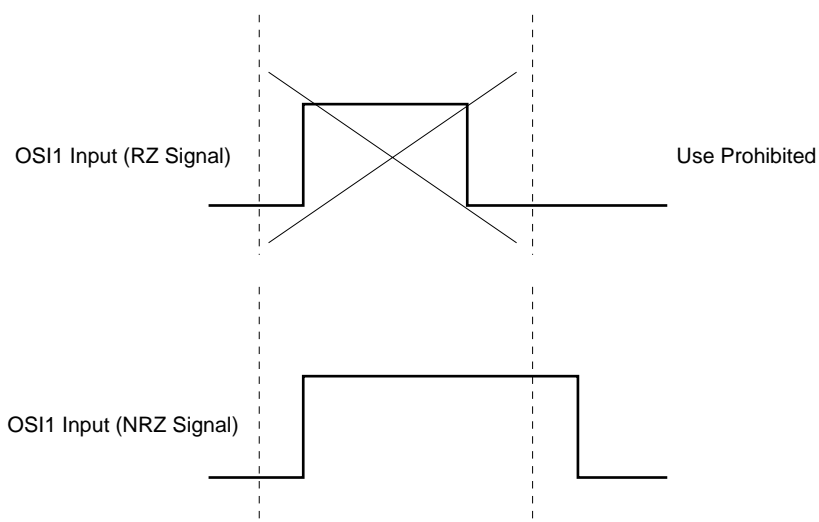
Error

(1) Oscillator circuits (Under development)

Oscillator circuits cannot be actually oscillated and tested with a simulator and LSI tester. Input a dummy signal to the input pin of the oscillator.

Use the inverted signal of the input signal as the expected value of the output of the oscillator. Be sure to input an NRZ signal ^{Note} as the input signal to the oscillation circuit with a test pattern for DC testing.

Figure 6-3. Oscillator Circuit Pattern



Note NRZ (No Return to Zero) signal
A signal that changes only once in one test pattern (1 test rate).

Correction

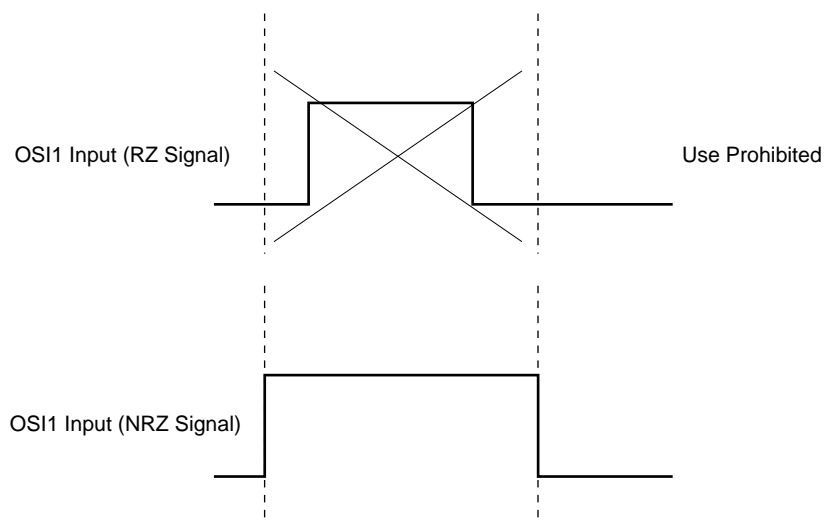
(1) Oscillator circuits

Oscillator circuits cannot be actually oscillated and tested with a simulator and LSI tester. Input a dummy signal to the input pin of the oscillator.

Use the inverted signal of the input signal as the expected value of the output of the oscillator. Be sure to input an NRZ signal ^{Note} without modulation as the input signal to the oscillator for all user patterns (function ,DC etc.).

Inputting undefined ("X") as the expected value of the oscillator output is prohibited in all user patterns.

Figure 6-3. Oscillator Circuit Pattern



Note NRZ (No Return to Zero) signal
A signal that changes only once in one test pattern (1 test rate).