

➤ Functions

- Host Interface
 - compliant with PCI 2.2
- Link layer
 - compliant with IEEE1394a-2000
- Physical layer
 - compliant with IEEE1394a-2000
 - three ports 400/200/100Mbps
- Stream Interface (MPEG2-TS)
 - four serial transport streams (combination: two transmit plus two receive, three transmit plus one receive)
- Functional Specification
 - compliant with DTCP1.30 specification
 - Partial TS function
 - On-chip DV decoder (It must be more than 16Mbit SDRAM)

- Power supply voltage
 - Internal $1.5V \pm 0.15V$
 - Peripheral $3.3V \pm 0.3V$
- Package
 - 305-pin FBGA 0.8mm pitch 17mm

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