

<Annex>

Specifications

Model name		V850ES/JG3-H			
		μPD70F3760	μPD70F3761	μPD70F3762	μPD70F3770
Internal memory	Flash memory (KB)	256	384	512	256
	RAM (KB)	40	48	56	40
	Data memory (KB)	8	8	8	8
Clocks	Main system clock	24 to 48 MHz			
	Sub system clock	32.768 kHz			
	Watchdog timer internal oscillator clock	220 kHz			
	Min. instruction execution time	20.8 ns (at 48 MHz)			
I/O ports		75			
Timers	16-bit timers	13			
	Realtime counter	1			
	Watchdog timer	1			
10-bit A/D converters		12			
8-bit D/A converters		2			
Serial interfaces	CSI	2			2
	CSI/UART	2			2
	CSI/UART/I ² C bus	1			1
	UARTA/I ² C bus	2			1
	UARTA/I ² C bus/CAN	—			1
USB controller		USB 2.0 function controller (full speed): 1			
DMA controllers		4			
Interrupt sources	External	17			
	Internal	69			73
Other functions		Low-voltage detection circuit, real time output, motor control timer, key return function, etc.			
On-chip debugging		Yes			
Operating power supply voltage		2.85 to 3.6 V @ 48 MHz			
Package		100-pin LQFP (14x14 mm)			

Model name		V850ES/JH3-H			
		μPD70F3765	μPD70F3766	μPD70F3767	μPD70F3771
Internal memory	Flash memory (KB)	256	384	512	256
	RAM (KB)	40	48	56	40
	Data memory (KB)	8	8	8	8
Clocks	Main system clock	24 to 48 MHz			
	Sub system clock	32.768 kHz			
	Watchdog timer internal oscillator clock	220 kHz			
	Min. instruction execution time	20.8 ns (at 48 MHz)			
I/O ports		96			
Timers	16-bit timers	13			
	Realtime counter	1			
	Watchdog timer	1			
10-bit A/D converters		12			
8-bit D/A converters		2			
Serial interfaces	CSI	2		2	
	CSI/UART	2		2	
	CSI/UART/I ² C bus	1		1	
	UARTA/I ² C bus	2		1	
	UARTA/I ² C bus/CAN	—		1	
USB controller		USB 2.0 function controller (full speed): 1			
DMA controllers		4			
Interrupt sources	External	20			
	Internal	69		73	
Other functions		Low-voltage detection circuit, real time output, motor control timer, key return function, etc.			
On-chip debugging		Yes			
Operating power supply voltage		2.85 to 3.6 V @ 48 MHz			
Package		128-pin LQFP (14x20 mm)			

Model name		V850ES/JG3-U		V850ES/JH3-U	
		μPD70F3763	μPD70F3764	μPD70F3768	μPD70F3769
Internal memory	Flash memory (KB)	384	512	384	512
	RAM (KB)	48	56	48	56
	Data memory (KB)	8	8	8	8
Clocks	Main system clock	24 to 48 MHz			
	Sub system clock	32.768 kHz			
	Watchdog timer internal oscillator clock	220 kHz			
	Min. instruction execution time	20.8 ns (at 48 MHz)			
I/O ports		75		96	
Timers	16-bit timers	13			
	Realtime counter	1			
	Watchdog timer	1			
10-bit A/D converters		12			
8-bit D/A converters		2			
Serial interfaces	CSI	2			
	CSI/UART	2			
	CSI/UART/I ² C bus	1			
	UARTA/I ² C bus	2			
USB controllers		USB 2.0 function controller (full speed): 1 USB 2.0 host controller (full speed): 1			
DMA controllers		4			
Interrupt sources	External	15		20	
	Internal	72		72	
Other functions		Low-voltage detection circuit, real time output, motor control timer, key return function, etc.			
On-chip debugging		Yes			
Operating power supply voltage		2.85 to 3.6 V @ 48 MHz			
Package		100-pin LQFP (14x14 mm)		128-pin LQFP (14x20 mm)	